

In the Claims:

Please amend claims 1-2 and 5-7 as indicated below. This listing of claims replaces all prior versions.

1. (Currently amended) A method of ~~determining-ating~~ a disconnection time information which is significant for a disconnection period in which disconnection period an integrated circuit of a data carrier designed for contactless communication with a communication partner device has not been adequately supplied with power by means of a power supply field, wherein at least one first storage capacitor of the integrated circuit is charged while the integrated circuit is being adequately supplied, and wherein the at least one first storage capacitor is discharged from a first starting time when the integrated circuit is subsequently no longer adequately supplied, and wherein the disconnection time information is determined on the basis of the discharge behavior, which is affected by the IC material and by radiation, of the at least one first storage capacitor and wherein the determined disconnection time information is corrected in dependence on the effects of the IC material and/or on at least one radiation effect.
2. (Currently amended) A method as claimed in claim 1, wherein the disconnection time information is determined and corrected on the basis of the discharge behavior of the at least one first storage capacitor and on the basis of the discharge behavior of a second storage[[,]] capacitor of the integrated circuit, wherein a renewed charging of the at least one first storage capacitor is prevented from a second starting time following the first starting time, from which second starting time an adequate supply is re-established, to a determination time and wherein the second storage capacitor is charged from the second starting time and wherein the second storage capacitor is discharged from a third starting time following the second starting time and wherein the discharge voltage of the at least one first storage capacitor is compared to the discharge voltage of the second storage capacitor at the determination time following the third starting time and wherein the disconnection time information is determined in dependence on a result of the comparison.

3. (Previously presented) A method as claimed in claim 1, wherein the disconnection time information is determined and corrected on the basis of the discharge behavior of the at least one first storage capacitor wherein the first storage capacitor is charged from a second starting time following the first starting time from which second starting time an adequate supply is re-established, and wherein the first storage capacitor is discharged from a third starting time following the second starting time and wherein the discharge voltage of the first storage capacitor is compared to the discharge voltage of the second storage capacitor present at the second starting time at the determination time following the third starting time and wherein the disconnection time information is determined in dependence on a result of the comparison.
4. (Previously presented) A method as claimed in claim 1, wherein the disconnection time information is used to decide whether the data carrier is to respond to certain prompt commands of the communication partner device.
5. (Currently amended) An integrated circuit of a data carrier designed for contactless communication with a communication partner device, comprising a first charging circuit for charging at least one first storage capacitor of the integrated circuit while the integrated circuit is being adequately supplied by means of a power supply field, and comprising a first discharge circuit for discharging the first storage capacitor following a first starting time when the integrated circuit is no longer adequately supplied with power, y of the integrated circuit from a first starting time wherein the discharge behavior of the at least one storage capacitor is affected by the IC material and by at least one radiation effect, and comprising determination logic circuitry means for determining ating a disconnection time information which is significant for a disconnection period in which disconnection period an the integrated circuit has not been adequately supplied with power, the disconnection time information being determined on the basis of the discharge behavior of the at least one first storage capacitor which is affected by the IC material and by the at least one radiation effect, so that the disconnection time information is available from a determination time (t4), and comprising correction logic circuitry means for the correction of the determined disconnection time information in

dependence on the effects of the IC material and/or the at least one radiation effect.

6. (Currently amended) An integrated circuit as claimed in claim 5, wherein a renewed charging of the at least one first storage capacitor is prevented with the aid of the determination logic circuitry ~~means~~ from a second starting time following the first starting time from, which second starting time an adequate supply is re-established, to a determination time and wherein a second storage capacitor is provided, and wherein a second charging circuit is provided for charging the second storage capacitor from the second starting time and wherein a second discharge circuit is provided for discharging the second storage capacitor from a third starting time following the second starting time wherein the discharge behavior of the second storage capacitor is affected by the IC material and by the at least one radiation effect, and wherein the determination logic circuitry ~~is means~~ ~~are~~ designed for comparing the discharge voltage of the at least one first storage capacitor to the discharge voltage of the second storage capacitor at the determination time (t4) following the third starting time and for determining the disconnection time information in dependence on a result of the comparison.
7. (Currently amended) An integrated circuit as claimed in claim 5, wherein a renewed charging of the at least one first storage capacitor can be started with the aid of the determination logic circuitry ~~means~~ from a second starting time following the first starting time from which second starting time an adequate supply is re-established, and wherein the first discharge circuit is provided for discharging the first storage capacitor from a third starting time following the second starting time wherein the determination logic circuitry ~~is means~~ ~~are~~ designed for comparing the discharge voltage of the first storage capacitor present at the second starting time to the discharge voltage of the first storage capacitor present at the ~~second starting time~~ at the determination time following the third starting time and for determining the disconnection time information in dependence on a result of the comparison.
8. (Previously presented) An integrated circuit as claimed in claim 6, wherein the capacitance of the at least one first storage capacitor corresponds to a multiple of the

capacitance of the second storage capacitor.

9. (Previously presented) An integrated circuit as claimed in claim 6, wherein the at least one first storage capacitor and the second storage capacitor are arranged immediately adjacent to one another in the integrated circuit.
10. (Previously presented) A data carrier for contactless communication with a communication partner device, which data carrier is provided with an integrated circuit as claimed in claim 5.